

ONE MONTH SUMMER TRAINING/INTERSHIP PROGRAM ON VLSI DESIGN -2019 (STP-VD)
(4th June to 3rd July 2019)

The department of Electronics & Communication Engineering is organizing a one month summer training program on "VLSI Design-2019" from **4th June to 3rd July 2019**. This training program will cover analog and digital integrated circuit design using commercial technology kits and simulation tools along with exposure to fabrication methods and PCB design. The course will cover theoretical sessions as well as hands on experience on microelectronics & VLSI design related state of art tools like, Xilinx, Synopsys/Cadence, EdwinXP, FPGA. More emphasis on laboratory work will be given during the training. In addition to this, physical design and verification of VLSI systems will also be covered.

The course is designed for **(2nd Year/3rd Year)** undergraduate students interested in pursuing training/ internship in the area of Microelectronics & VLSI Design.

- (1) **Eligibility:** B.Tech./B.E. (Electronics / Electronics & Communication/ Electronics & Telecommunication/ Electrical & Electronics/ Electrical Engineering or similar discipline) **Second Year and Third Year Students** of any UGC/ AICTE recognized University/ Institute/ College.
- (2) **Fees & mode of payment:** Course fee is 10,000/- (Rupees Ten Thousands only- **including registration fees and hostel accommodation**). The course fee should be paid through a Demand Draft, in favor of "The Director, IIT Allahabad" payable at Allahabad. The amount may also be payable through internet banking. The details are

Account Name: Indian Institute of Information Technology
Account Number: 30996838478
IFSC Code: SBIN0010891
Bank & Branch: SBI, Jhalwa, Allahabad.

The course fee is non-refundable. Participants have to pay Nominal food charges for lunch and dinner

- (3) **Duration:** One month.
- (4) **Application Procedure:** Application form will be available on the stpvd.iiita.ac.in or in the link <https://docs.google.com/forms/d/e/1FAIpQLSdabequOGUM6bFsyYFuS6g7LYwK3BWs2Cu0VPzvkJeGBPC4jg/viewform>. The applicants are required to fill the google form by depositing the required fees through DD/Internet banking.
- (5) **No of Seats:** Limited (on first come first serve basis)

REGISTRATION FEE:

Rs 10,000 /-

IMPORTANT DATES:

Issue of Application form link:

30th March 2019

Last date of receipt of Application form along with fees:

15th May 2019

CONTACT ADDRESS:

Coordinators STP-VD, VLSI Design-2018, Room No 2106, CC-1, Department of Electronics and Communication Engineering, Indian Institute of Information Technology Allahabad, -211012, Uttar Pradesh,

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